

11/13/00

Please type a plus sign (+) inside this box → ☐

Approved for use through 09/30/01  
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no person is required to respond to a collection of information unless it displays a valid OMB control number.

<b>UTILITY PATENT APPLICATION TRANSMITTAL</b> <i>Only for new nonprovisional applications under 37 CFR 1.53(b)</i>		Attorney Docket No. <b>06192.0166.NPUS00</b>
First Named Inventor or Application Identifier		JANG, Yong-Kyu
Title	Reflective-Transmission Type Thin Film Transistor Liquid Crystal Display	
Express Mail Label No.		

<b>APPLICATION ELEMENTS</b> <i>See MPEP chapter 600 concerning utility patent application contents</i>	Assistant Commissioner for Patents <b>ADDRESS TO:</b> Box Patent Application Washington, DC 20231
---	---

- |   |   |
|---|---|
| <p>1. <input type="checkbox"/> *Fee Transmittal Form (Form PTO-1082)<br/><i>(Submit an original and a duplicate for fee processing)</i></p> <p>2. <input checked="" type="checkbox"/> Specification [Total Pages <b>19</b> ]<br/><i>(preferred arrangement set forth below)</i></p> <ul style="list-style-type: none"><li>- Descriptive title of the Invention</li><li>- Cross References to Related Applications</li><li>- Statement Regarding Fed sponsored R&amp;D</li><li>- Reference to Microfiche Appendix</li><li>- Background of the Invention</li><li>- Brief Summary of the Invention</li><li>- Brief Description of the Drawings (if filed)</li><li>- Detailed Description</li><li>- Claims</li><li>- Abstract of the Disclosure</li></ul> <p>3. <input checked="" type="checkbox"/> Drawing(s) (35 USC 113) [Total Sheets <b>10</b> ]</p> <p>4. <input type="checkbox"/> Oath or Declaration [Total Pages <input type="text"/> ]</p> <ul style="list-style-type: none"><li>a. <input type="checkbox"/> Newly executed (original or copy)</li><li>b. <input type="checkbox"/> Copy from a prior application (37 CFR 1.63(d))<br/><i>(for continuation/divisional with Box 17 completed)</i><br/><i>[Note Box 5 below]</i></li><li>i. <input type="checkbox"/> <b>DELETION OF INVENTOR(S)</b><br/>Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).</li></ul> <p>5. <input type="checkbox"/> Incorporation By Reference <i>(useable if Box 4b is checked)</i><br/>The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.</p> | <p>6. <input type="checkbox"/> Microfiche Computer Program <i>(Appendix)</i></p> <p>7. Nucleotide and/or Amino Acid Sequence Submission <i>(if applicable, all necessary)</i></p> <ul style="list-style-type: none"><li>a. <input type="checkbox"/> Computer Readable Copy</li><li>b. <input type="checkbox"/> Paper Copy (identical to computer copy)</li><li>c. <input type="checkbox"/> Statement verifying identity of above copies</li></ul> |
|---|---|

<b>ACCOMPANYING APPLICATION PARTS</b>	
8. <input type="checkbox"/> Assignment Papers (cover sheet & document(s))	
9. <input type="checkbox"/> 37 CFR 3.73(b) Statement <input type="checkbox"/> Power of Attorney <i>(when there is an assignee)</i>	
10. <input type="checkbox"/> English Translation Document <i>(if applicable)</i>	
11. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations	
12. <input type="checkbox"/> Preliminary Amendment	
13. <input checked="" type="checkbox"/> Return Receipt Postcard (MPEP 503) (Two) <i>(should be specifically itemized)</i>	
14. <input type="checkbox"/> *Small Entity Statement(s) <input type="checkbox"/> Statement filed in prior application, Status still proper and desired	
15. <input type="checkbox"/> Certified Copy of Priority Document(s) <i>(if foreign priority is claimed)</i>	
16. <input type="checkbox"/> Other:	

\*NOTE FOR ITEMS 1 & 14: IN ORDER TO BE ENTITLED TO PAY SMALL ENTITY FEES, A SMALL ENTITY STATEMENT IS REQUIRED (37 C.F.R. § 1.27). EXCEPT IF ONE FILED IN A PRIOR APPLICATION IS RELIED UPON (37 C.F.R. § 1.28).

17. If a **CONTINUING APPLICATION**, check appropriate box and supply the requisite information:

<input type="checkbox"/> Continuation	<input type="checkbox"/> Divisional	<input type="checkbox"/> Continuation-in-part (CIP)	of prior application No. <input type="text"/>
Prior Application Information: Examiner: <input type="text"/>		Group/Art Unit: <input type="text"/>	

**18. CORRESPONDENCE ADDRESS**

☒ Customer Number or Bar Code Label 22930 or ☐ Correspondence address below  
*(Insert Customer No. or Attach bar code label here)*

NAME	HOWREY SIMON ARNOLD & WHITE, LLP				
Box No.	34				
ADDRESS	1299 Pennsylvania Avenue, N.W.				
CITY	Washington	STATE	DC	ZIP CODE	20004-2402
COUNTRY	US	TELEPHONE	202-783-0800	FAX	202-383-7195
Name (Print/Type)	Michael J. Bell		Registration No. (Attorney/Agent)		39,604
Signature	Michael J. Bell by David R. Marsh		Date	November 13, 2000	

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

Reg. No. 41,408 (DAVID R. MARSH)



1299 PENNSYLVANIA AVE., NW  
WASHINGTON, DC 20004-2402  
PHONE 202.783.0800  
FAX 202.383.6610  
A LIMITED LIABILITY PARTNERSHIP

November 13, 2000

*Box Patent Application*

**BY HAND DELIVERY**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Re: Non-Provisional Utility Patent Application  
Application No.: To be Assigned; Filed: November 13, 2000  
For: **Reflective-Transmission Type Thin Film Transistor Liquid Crystal Display**  
Inventor(s): **Yong-Kyn, JANG; Young-Kuil, JOO; and Myeon-Koo, KANG**  
Our Ref: 06192.0166.NPUS00

Sir:

The following documents are forwarded herewith for appropriate action by the U.S. Patent and Trademark Office:

1. Utility Patent Application Transmittal Form;
2. U.S. Utility Patent Application entitled:

**Reflective-Transmission Type Thin Film Transistor Liquid Crystal Display**

and naming as inventor:

**Yong-Kyn, JANG; Young-Kuil, JOO; and Myeon-Koo, KANG**

the application consisting of:

- a. a specification containing:
  - (i) 19 pages of description prior to the claims;
  - (ii) 5 pages of claims (16 claims); and
  - (iii) a one (1) page abstract;
- b. 10 sheets of drawings. (Figs. 1-3, 14A, 14B, and 15-19A, inclusive)



Assistant Commissioner for Patents  
November 6, 2000  
Page 2

3. Two (2) return postcards.

It is respectfully requested that, of the two attached postcards, one be stamped with the filing date of these documents and returned to our courier, and the other, prepaid postcard, be stamped with the filing date and unofficial application number and returned as soon as possible.

Applicant hereby claims foreign priority benefits under Title 35, United States Code, § 119 to Korean Applications Nos. 1999-49940 filed November 11, 1999, and 2000-11533 filed March 8, 2000.

This application is being filed under 37 C.F.R. § 1.53(b) without an executed Declaration.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 08-3038. A duplicate copy of this letter is enclosed.

Respectfully submitted,

*Michael J. Bell*

Michael J. Bell  
Registration No. 39,604

Enclosures

by  
*David R. Marsh*

Reg. No

41,408

(DAVID R. MARSH)

# REFLECTIVE-TRANSMISSION TYPE THIN FILM TRANSISTOR LIQUID CRYSTAL DISPLAY

5

## Field of the Invention

The present invention is related to reflective-transmission type thin film transistor liquid crystal display and more specifically related to liquid crystal display wherein the reflective pixel electrode and transmissive pixel electrode composing the whole pixel electrode can be stably connected with the source electrode of thin film transistor which controls a pixel.

10

## Background of the Invention

Liquid crystal displays can be classified to many numerous types according to the methods of manufacturing and the structures. For example, top gate type and bottom gate type can be differentiated. For the manufacturing of top gate type LCD, active layer made with semiconductor material is formed before gate insulating layer and gate pattern are formed. But, in case of bottom gate type LCD, the gate pattern and the gate insulating layer is formed prior to the active layer.

15

On the other hand, the classification of reflective type and transmissive type can be possible. In the transmissive type LCD, the pixel electrode is made with reflective materials like metals which reflect light from external environment. But, in the reflective type LCD, the pixel electrode is made with transparent conductive materials containing indium metal oxide lineage like ITO (indium tin oxide). In the transmissive type LCD,

20

independent light source is placed at the backside of LCD panel and thus called backlight, and various colorful images can be presented by controlling the state of liquid crystal of numerous pixels to transmit or to shield the light from the backlight.

Recently, reflective type LCD is considered as an alternative in notebook LCD which need large display size and high quality image. And reflective-transmission type LCD which has merits of both reflective type and transmission type is also presented by SHARP(registered trade mark).

In the reflective transmission type LCD of SHARP, transmissive pixel electrode is formed to be connected to source electrode of thin film transistor in the process of making thin film transistor on glass substrate. And passivation layer which has contact hole in the source region of the thin film transistor is formed on the thin film transistor. Then, reflective metal layer like aluminum is formed on the passivation layer and patterned to confine the reflective area and to form a window which reveals some part of transmissive layer. Here, the patterned reflective metal, the reflective pixel electrode is connected to the source electrode through the contact hole.

Meanwhile, in the process of fabricating the reflective transmission type LCD, some problems can be caused according to the materials adopted as transmissive pixel electrode and reflective pixel electrode. For example, if the reflective pixel electrode made with aluminum, is electrically connected with the source electrode by the transmissive pixel electrode which covers the source electrode. But, the transmissive pixel electrode is made with ITO (indium tin oxide) generally used as transparent conduction layer, an oxide insulation layer is to be formed between the reflective pixel electrode and transmissive pixel electrode . Then, the oxide layer prevents the

enhancement of electric field to the reflective pixel electrode.

To avoid the problem, a parameter conduction layer can be used at the interface of the two problematic electrode materials. Also, the two problematic pixel electrode may be connected to the source electrode at different positions to prevent the problem.

- 5 However, compared with the relatively large pixel electrode area, the region of the source electrode is confined to relatively small area, which makes it difficult to design and to form the transmissive area and the reflective area properly and simply.

### Summary of the Invention

- 10 It is therefore an object of the present invention to provide a reflective transmission type TFT LCD wherein the stable operation of whole pixel electrode and safe enhancement of electric potential to whole pixel electrode can be confirmed.

- It is another object of the present invention to provide a reflective transmission type TFT LCD wherein the easiness and simplicity can be sustained in the process of forming the whole pixel electrode.
- 15

- In order to obtain the above mentioned and other objects, the present invention provide a reflective transmission type TFT LCD wherein each of the reflective pixel electrode and the transmissive pixel electrode is connected directly to a source electrode of a thin film transistor, or the transmissive pixel electrode are concurrently formed with gate electrode and made with double layer of transparent conduction layer and metal layer which can be used as parameter conduction layer between the transparent conduction layer and the reflective pixel electrode.
- 20

According to one aspect of the present invention, the reflective transmission

type thin film transistor liquid crystal display (TFT LCD) comprises a glass substrate, at least one thin film transistor on the substrate for controlling a pixel, passivation layer having at least one contact hole in a source region of the thin film transistor, a transmissive pixel electrode which is formed on the passivation layer and is connected with a source electrode of the source region through a contact hole, a reflective pixel electrode which is formed on the passivation layer and is connected with the source electrode of the source region through a contact hole. And the pixel area is composed of a transparent area in which only the transmissive pixel electrode of whole pixel electrode exist and a reflective area in which the reflective pixel electrode exist.

Here, it is obvious to those who are skilled in the art that the structure of thin film transistor can be varied according to the method of fabrication.

Here, for the prevention of defective chemical reaction among the reflective layer, the transmissive layer and the etchant in the process of etching or cleaning, the reflective transmission type thin film transistor liquid crystal display may have a separating insulator layer which have good transparency between the transmissive pixel electrode and the reflective pixel electrode.

If the number of contact hole is one, the transmissive pixel electrode (in case of comprising the separating insulator, the separating insulator also) have a hole revealing some part of the source region and the reflective pixel electrode which is formed over the transmissive pixel electrode is connected with the source electrode of the part of the source region via the hole.

In case that the number of contact hole is two, one is for the transmissive pixel electrode and the other is for the transmissive pixel electrode, in the region of the other

contact hole, the transmissive pixel electrode (in case of having a separating insulator, the separating insulator also) has a hole revealing the source electrode for a connection between the reflective pixel electrode and the source electrode.

Here, the hole formed in the transparent conduction layer and in the separating  
5 insulator can be formed by sequential forming of the two layers and patterning of the two layers using one photo mask. Generally, the width of the contact hole formed in the source region is far wider than the thickness of layers forming the pixel electrode, the removal of some part of the transmissive conductor layer and the separating insulator at the contact hole can be executed in the patterning.

10 In the present invention, the reflective pixel electrode may be made with metals including aluminum and the transmissive pixel electrode may be made with indium metal oxide lineage such as ITO, IZO.

Generally, the transparent area takes form and shape of window in the reflective  
area. And, the separating insulator can be removed for the efficiency of transmission of  
15 backlight by etching process wherein the reflective pixel electrode acts as an etching mask.

In the above explanation, for the efficiency of reflection, the transmissive pixel  
electrode are preferably formed before the reflective pixel electrode are formed. But, the  
sequence of forming the transmissive pixel electrode and forming the reflective pixel  
20 electrode can be changed with no significant change of effect.

According to another aspect of the present invention, the reflective transmission  
type thin film transistor liquid crystal display (TFT LCD) comprises a glass substrate, at  
least one thin film transistor on the substrate for controlling a pixel, a first type pixel



electrode which is formed and patterned concurrently with gate of the thin film transistor, comprising upper non-oxidizing metal layer removed in transparent area and lower transparent conductor layer. It also comprises a passivation layer which is formed over the thin film transistor and over a first type pixel electrode. The passivation layer has a contact hole at a source region and a hole (or a window) in the transparent area and neighboring region (in a region including the transparent area). A second type pixel electrode which is formed over the passivation layer, is patterned to cover whole pixel area except the transparent window region, and is connected with a source electrode of the thin film transistor through the contact hole and connected with the non-oxidizing metal layer of the first type pixel electrode at the neighboring region.

Here, the passivation layer is preferably made with photo-sensitive transparent insulator and at least at some part the surface of the passivation layer is treated to take embossment which forms micro lens for focusing or integrating light. More preferably, the thickness of the passivation layer times the difference of refraction index corresponds to a quarter of wavelength of light in liquid crystal.

And the non-oxidizing metal can be one selected from the group consisting of chromium and tungsten-molybdenum.

#### Brief Description of the Drawings

The other objects, features and advantages of the present invention will become apparent from the following description taken in conjunction with the accompanying drawings, in which:

Fig. 1 to Fig. 7 are cross sectional views to selected stages of a method for

forming an example according to one aspect of the present invention;

Fig. 8 and Fig. 9 are cross sectional views to selected stages of a method for forming other example of the aspect of the present invention;

Fig. 10 and Fig. 11 are cross sectional views to selected stages of a method for forming another example of the aspect of the present invention;

Fig. 12 shows a cross sectional view of a different example of the aspect of the present invention;

Fig. 13 shows a planar view of the example of Fig. 12;

Fig 14a shows a cross sectional view of other different example of the aspect of the present invention;

Fig. 14b shows a cross sectional view schematically presenting the phase change of light in an example of the present invention.

Fig. 15 shows the planar view of an example of the other aspect of the present invention.

Fig. 16 shows the cross sectional view of the example of Fig. 15 according to the cut line AA'.

Fig. 17 to Fig. 19 are cross sectional views of different examples of the other aspects of the present invention.

### Detailed Description of the Preferred Embodiment

The present invention will now be described hereinafter more fully with reference to the attached drawings, wherein preferred embodiment of the invention are shown. This invention may, however, be embodied in different forms and should not be

construed as limited to the embodiments set forth herein. Rather, these embodiments are provided so that this disclosure will be through and complete, and will fully convey the scope of the invention to those skilled in the art.

(Embodiment 1)

5 According to Fig. 1 to Fig. 3, gate electrode 11 is formed on a substrate 10. Gate insulating layer 13, active layer 15 made with amorphous silicon and Ohmmic contact layer 14 made with doped amorphous silicon are sequentially formed on the substrate 10 including the gate electrode 11. By patterning process, the Ohmmic contact layer 14 and active layer 15 are patterned to define active region. Then, metal layer is formed and patterned to shape a source electrode 16 and a drain electrode. In the patterning process, the Ohmmic contact layer and upper part of active layer 15 in the active region are also removed to make channel of a thin film transistor. As shown in Fig. 3, the source/drain electrode is larger than the source/drain region of the active region.

15 Referring to Fig. 4, a passivation layer 17 made with insulating layer is formed on the source/drain electrode and patterned to form contact hole revealing some part of the source electrode 16.

Referring to Fig. 5, on the passivation layer 17, a transmissive pixel electrode 18 is formed to cover a transparent area. The transmissive pixel electrode 18 can be made by depositing (including physical depositing) the transparent conduction layer like ITO and patterning the transparent conduction layer. In the patterning process, the transparent conduction layer is removed at some part of contact hole to make a hole revealing some part of the source electrode.

With regard to Fig. 6 and Fig. 7, separating insulator 19 made with silicon nitride is formed on the substrate including transmissive pixel electrode 18. In the separating insulator 19, a hole is formed to reveal the part of the source electrode 18. Accordingly, when the reflective conduction layer made with aluminum neodymium is deposited on the separating insulator 19, the reflective conduction layer is connected directly to the source electrode 18 through the hole formed in the separating insulator 19 and transmissive pixel electrode 18. Then, by patterning the reflective conduction layer, the reflective pixel electrode 20 having window through which the backlight is transmitted can be formed. For the efficiency of light transmission in the transparent area, the separating insulator can be removed in the window area by using the reflective pixel electrode 20 as an etching mask.

#### (Embodiment 2)

According to Fig. 8, a structure of a thin film transistor and passivation layer 17 are formed on a substrate 10. Then, reflective conduction layer made with aluminum containing metal is formed first on the passivation layer 17 which has contact hole. Through the contact hole the reflective conduction layer is connected to the source electrode 16. By patterning the reflective conduction layer, reflective pixel electrode 20 having light transmission window is formed and concurrently a hole is formed at some part of the contact hole. Then, transparent conduction layer made with IZO is deposited (for the present invention, sputtering may be a type of physical deposition) and patterned to form a transmissive pixel electrode.

Compared with ITO layer, the IZO layer has less harmful reaction with aluminum

containing metals at the interface. So, separating insulator can be omitted. However, if the sequence of forming reflective pixel electrode and forming transmissive pixel electrode is reversed, the IZO layer is to be damaged when the aluminum containing layer is patterned to form a light transmission window. Because the etching selectivity for the two layer is not so different.

### (Embodiment 3)

Fig. 10 and Fig. 11 show another embodiment that is slightly changed from the embodiment 1. In the embodiment 1, according to Fig. 5 And Fig. 6, the removal of transparent conduction layer and separating insulator layer in a contact hole region is made by individual patterning. However, in this embodiment, the hole formed in the transparent conduction layer and in the separating insulator layer is formed by sequential forming of the two layers and by patterning the two layers using one photo mask. Then, reflective pixel electrode 20 is formed. After the patterning of reflective conduction layer, the separating insulator 19 can be etched in the transparent window area for the improvement of efficiency of light transmission.

### (Embodiment 4)

Fig. 12 shows another embodiment of the present invention. A source electrode 16 is extended beyond the active region made with amorphous silicon layer. In the region of the source electrode 16, two contact holes are formed in a passivation layer 17 which covers the source electrode 16. Then, transmissive pixel electrode 18 made with ITO is formed on the passivation layer 17. Next, a separating insulator 19 made

with silicon nitride or organic insulation material is formed on the transmissive pixel electrode 18 to cover the transmissive pixel electrode 18. In the process of forming, the transparent conduction layer and the separating insulator layer is removed at one contact hole region. And then, reflective conduction layer is formed on the separating insulator 19 and patterned to form a reflective pixel electrode 20 which has light transmission window. Thus, the transmissive pixel electrode 18 is connected to the source electrode 16 through the other contact hole. And the reflective pixel electrode 20 is connected to the source electrode 16 through the one contact hole.

Fig. 13 is a planar view to the embodiment shown in Fig. 12 and also can be a reference for the above described embodiments. In a source electrode region, there are two contacts through which pixel electrodes are individually connected to the source electrode. In the middle of a pixel region, two light transmission window 27 exist and form transparent area. Hereinafter more detailed description to the Fig 13 regarding above mentioned embodiments will be made.

On a substrate like glass substrate, a gate line 110 including gate electrode is formed with metal layer like chromium. Concurrently, a capacitor line 111 is also formed with the same metal layer. Then, gate insulation layer such as silicon nitride layer is formed on the gate line 110 and the capacitor line 111. And active layer pattern 150 made with amorphous silicon and Ohmic contact layer pattern made with N+ doped amorphous silicon is formed at least on the gate electrode. By depositing and patterning metal layer like chromium, titanium and molybdenum, a source electrode 162 and a drain electrode 161 which are connected with the Ohmic contact layer pattern, data line 160 crossing with the gate line 110 and the capacitor line 111 and metal pattern 163

formed on the capacitor line 111 is made. The metal pattern 163 associated with the capacitor line 111 makes an effective means for acquiring capacitance.

Then, passivation layer is formed over the source electrode 162 and the drain electrode 161. Regarding to embodiment 4 shown in Fig. 12, two contact holes 171,172 in the source electrode region and one contact hole 173 on the metal pattern 163 are formed by patterning the passivation layer. Contact holes 173,174 are also formed at the end of each data line 160 and gate line 110, i.e. at each data pad and gate pad.

In case of forming the passivation layer with organic insulator, at the surface of passivation layer, embossment can be made to be micro lens which integrates reflected light or transmitting light. And, if the passivation layer is photosensitive, patterning the passivation layer can be executed by only photolithography process and one etching step can be omitted.

Over the passivation layer, transparent conduction layer such as ITO, IZO is deposited and patterned to form a transmissive pixel electrode which is connected to the source electrode 162 through one contact hole 171. The transmissive pixel electrode 180 is preferably made small enough to cover only the transmissive area. The transmissive pixel electrode 180 is also connected with the metal pattern 163 through the contact hole 173 formed on the capacitor line 111 to be a kind of capacitor electrode. Also, the transparent conduction layer can be remained on the pad area where the remained transparent conduction layer act as subsidiary pad. On the transmissive pixel electrode 180, a separating insulator made with silicon nitride or organic insulator is formed. To reveal the source electrode 162 through the other contact hole 172, the separating insulator and the transmissive pixel electrode 180 have hole in them. Then,

the reflective pixel electrode 200 formed on the separation insulator is connected with the source electrode 162 through the other contact hole 172. The reflective pixel electrode 200 can be made with aluminum containing metal or silver containing metal and patterned to form reflective area which covers the whole pixel region except for the transparent area. The reflective area can be formed over the gate line 110 or the data line 160 to enlarge the whole pixel area.

Generally, the micro lens for integrating light can be made at the surface of passivation layer by forming embossments at the surface. If the separating insulator is formed with organic material, preferably with photo sensitive material like polyimide, the micro lens can also be made at the surface of the separating insulator in place of the passivation layer. And, in place of the transmissive pixel electrode 180, the reflective pixel electrode 200 can be connected to metal pattern 163 to become a capacitor electrode. It is obvious to those skilled in the art that, to the embodiment 1~3, the above description can be easily applied by mere change of contact between the source electrode and the pixel electrodes.

#### (Embodiment 5)

Fig 14a shows a different type of embodiment of the present invention. A source electrode 16 is formed wide on the substrate in the structure of the bottom gate type thin film transistor. A passivation layer 17 is formed on the substrate including the source electrode 16. A pixel electrode is formed on the passivation layer 17. The pixel electrode comprises a transmissive pixel electrode 18, a separating insulator 19 and a reflective pixel electrode 20. Both the transmissive and the reflective pixel electrode 18,20 are



individually connected to the source electrode 16 through a contact hole formed in the passivation layer 17. The separating insulator 19 is formed to prevent the problems caused by the direct connection of the transmissive pixel electrode 18 and the reflective pixel electrode 20. In the middle of a pixel area, there is a transparent area made by removing the reflective conduction layer. In the transparent area, there is a window formed by patterning the passivation layer 17. The window in the passivation layer is formed to improve the brightness of LCD. Generally, for the maximum brightness, the  $\Delta n d$  of liquid crystal layer in reflective type LCD correspond to a quarter of wavelength of light and the  $\Delta n d$  of liquid crystal layer in transmissive type LCD correspond to half of the wavelength of light. So, if the crystal layer has same thickness over the whole pixel area in a reflective-transmission type LCD like embodiment 4, the transmissive pixel area or the reflective pixel area cannot be in the state of maximum brightness in bright mood. Thus, by differentiating the thickness of liquid crystal layer in transmissive region from that in reflective area, the state of polarized light can be matched in phase with the polarizer film placed at the front side of LCD in bright mood. And at least the brightness of LCD can be improved by partial removal of the mismatch in phase. And, considering the above described, the thickness of passivation layer preferably is equal to the thickness of the liquid crystal layer in reflective pixel area. In Fig. 14b, the above explanation is digested.

#### (Embodiment 6)

Fig. 15 shows a planar view of pixel area and pad area in an embodiment of the other aspect of the present invention. The sequence of forming reflective pixel electrode

and forming transmissive pixel electrode can be exchanged. Along the peripheral part of transparent area, a reflective pixel electrode and a transmissive pixel electrode are connected each other by a parameter metal layer.

Fig. 16 shows a cross sectional view of the embodiment shown in Fig. 15 along the line AA'. To form the structure, first, transparent layer 51 and chromium layer 61 are made on the glass substrate 10 and patterned to form gate electrode, gate line, gate pad and transparent layer containing pixel electrode. The transparent conduction layer 51 can be physically deposited by sputtering ITO or IZO material, and the chromium layer 61 can also be formed by sputtering. The transparent layer containing pixel electrode is electrically separated with the gate electrode. The gate insulation layer 13 made with silicon oxide or silicon nitride is formed by CVD (chemical vapor deposition) method and patterned to cover the gate electrode and gate line. But the gate insulation layer is removed on the transparent layer containing pixel electrode. Amorphous silicon active layer 15 and impurity doped amorphous silicon ohmmic contact layer 14 are deposited and patterned to form active region. The active region can be divided into source, drain and channel region. Then, the drain electrode, the source electrode 16 and data line and data pad are formed by forming metal layer and patterning the metal layer. Ohmmic contact layer is etched by using the metal layer pattern like the source/drain electrode as an etching mask to make the channel operated properly. A passivation layer 17 made with organic insulator is formed over the source/ drain electrode and patterned to form a contact hole revealing a part of the source electrode 16 and a window. The window reveals the most part of the transparent layer containing pixel electrode including a transparent area and the neighboring part of the transparent

area.

The thickness of passivation layer 17 may preferably be equal to the thickness of the liquid crystal layer in reflective pixel area with no regard to the direction of robbing, the tilt angle and other variables of LCD.

5        And the surface of the passivation layer 17 may has embossment to form micro lenses to integrate light. Then, the reflective conduction layer including aluminum is formed over the passivation layer 17 and patterned to define the transparent area and to form reflective pixel electrode 20 which is connected to the source electrode 16 through the contact hole.

10        After the patterning the reflective conduction layer, the chromium layer 61, the upper layer of the transparent layer containing pixel electrode is removed by using the reflective pixel electrode 20 as an etching mask. Thus, the transparent conduction layer 51 in the transparent layer containing pixel electrode are indirectly connected at the neighboring part of the transparent area with the reflective pixel electrode 20 by the  
15        parameter chromium layer 61 and also electrically connected with the source electrode 16 by the reflective pixel electrode 20.

20        In the above embodiment, transparent layer containing pixel electrode may be formed with the source electrode in place of the gate electrode or may be formed with the same kind of double layers at a different step after the forming of the source electrode. So, if the transparent layer containing pixel electrode is formed with the source electrode in place of the gate electrode, gate electrode may be made with aluminum, the source/drain electrode layer may be made with transparent layer and chromium, and the reflective pixel electrode may be made with chromium or aluminum.

(Embodiment 7)

Fig. 17 shows a cross sectional view of a different embodiment of the present invention. Here, a part of thickness of a passivation layer 17 is remained in the transparent area. Whole pixel electrode comprises a transmissive pixel electrode 20 which is connected to a source electrode 16 with no connection through a contact hole. The whole pixel electrode also comprises a reflective pixel electrode 20 which is separated at the most part by passivation layer 20 and connected through a contact hole with the source electrode 16. Of course, the reflective conduction layer is removed in the transparent area so that the light generated from a backlight can be transmitted to the front side of LCD and the passivation layer should be transparent..

(Embodiment 8)

Fig. 18 shows a cross sectional view of an embodiment which can compensate the shortness of the embodiment shown in Fig. 17. In the embodiment shown in Fig. 17, it is difficult to remove the partial thickness of the passivation layer 17 in the transparent region in the process of patterning the passivation layer 17 to form a contact hole at some part of the source electrode region. But, in this embodiment, the passivation layer 17 is removed thoroughly in the transparent area and the transmissive pixel electrode 18 has covering 39. Here, The transmissive pixel electrode 18 including the covering 39 is formed by depositing a covering layer made with silicon oxide or silicon nitride on the transparent conduction layer and patterning the covering layer and the transparent conduction layer with an etching mask. By the covering 39, the transparent conduction

layer like ITO can be separated with the reflective conduction layer like aluminum in the process of fabrication.

#### (Embodiment 9)

5 According to Fig. 19, a transparent layer made with ITO and a metal layer are formed over a source electrode 16 and patterned to form a transparent layer containing pixel electrode. Then a passivation layer 17 is formed over the transparent layer containing pixel electrode. The passivation layer 17 is patterned to form a contact hole at a part of the source electrode 16 and to form a window revealing some part of the  
10 transparent layer containing pixel electrode. And, a reflective conduction layer made of aluminum is formed on the passivation layer 17 and patterned to define a reflective area. In the patterning of the reflective conduction layer, the reflective conduction layer is removed in the transparent area to reveal the transparent layer containing pixel electrode. Then, the upper layer of the transparent layer containing pixel electrode, the  
15 metal layer 28 is removed by using the reflective pixel electrode 20 as an etching mask. Compared with the embodiment shown in Fig. 18, the embodiment shown in Fig. 19 have a different covering for transmissive pixel electrode 18. The covering, the metal layer 28 is made of metal which can act as a buffer layer between the transparent conduction layer and the reflective conduction layer. And, finally, the covering is to be  
20 removed in the transparent area.

Compared with the embodiment 6 where the transparent layer containing pixel electrode is formed with the same layer for gate electrode, the transparent layer containing pixel electrode (made with transparent conduction layer and the covering

layer) of the present embodiment is formed concurrently with the same layers for the source electrode in place of the gate electrode or may be formed with the same kind of double layers at a different step after the forming of the source electrode. Then, the covering layer preferably is made with materials of good conductivity, metal, like chromium, titanium for the signal transmission of data line which is concurrently formed with the source electrode or the transparent layer containing pixel electrode.

The above mentioned embodiments are all related with amorphous silicon type and bottom gate type TFT LCD, but the concept of the present invention can also be applied to polycrystalline silicon type or top gate type TFT LCDs.

## WHAT IS CLAIMED IS:

1. A Reflective transmission type thin film transistor liquid crystal display (TFT LCD) comprising:

5 a glass substrate;  
at least one thin film transistor on the substrate for controlling a pixel;  
a passivation layer having at least one contact hole in a source region of the thin film transistor;

a transmissive pixel electrode which is formed on the passivation layer and is  
10 connected with a source electrode of the source region through a contact hole; and

A reflective pixel electrode which is formed on the passivation layer and is connected with the source electrode of the source region through a contact hole;

wherein pixel area is composed of a transparent area in which only the transmissive pixel electrode of whole pixel electrode exist and a reflective area in which  
15 the reflective pixel electrode exist.

2. The reflective transmission type thin film transistor liquid crystal display of claim 1, further comprising:

a separating insulator between the transmissive pixel electrode and the  
20 reflective pixel electrode.

3. The reflective transmission type thin film transistor liquid crystal display of claim 2, wherein the number of contact hole is one, the transmissive pixel electrode

and the separating insulator have a hole revealing some part of the source region and the reflective pixel electrode which is formed over the transmissive pixel electrode is connected with the source electrode of the part of the source region via the hole.

5           4.       The reflective transmission type thin film transistor liquid crystal display of claim 2, wherein the number of contact hole is two, one for the transmissive pixel electrode and the other for the transmissive pixel electrode, and in the source region of the other contact hole, the transmissive pixel electrode and the separating insulator have a hole revealing for a connection between the reflective pixel electrode and the  
10   source electrode.

          5.       The reflective transmission type thin film transistor liquid crystal display of claim 1, wherein the number of contact hole is one, the transmissive pixel electrode and the separating insulator have a hole revealing some part of the source region and  
15   the reflective pixel electrode which is formed over the transmissive pixel electrode is connected with the source electrode of the part of the source region via the hole.

          6.       The reflective transmission type thin film transistor liquid crystal display of claim 1, wherein the reflective pixel electrode has a hole revealing some part of the  
20   source region and the transmissive pixel electrode which is formed over the reflective pixel electrode is connected with the source electrode of the part of the source region via the hole.



7. The reflective transmission type thin film transistor liquid crystal display of claim 6, further comprising:

a separating insulator having a connection hole connected with the hole between the transmissive pixel electrode and the reflective pixel electrode.

5

8. The reflective transmission type thin film transistor liquid crystal display of claim 1, wherein the reflective pixel electrode is made with metals including aluminum and the transmissive pixel electrode is made with indium metal oxide lineage.

9. The reflective transmission type thin film transistor liquid crystal display of claim 1, wherein the transparent area takes form and shape of window of the reflective area.

10. The reflective transmission type thin film transistor liquid crystal display of claim 9, wherein a separation insulator existing between the reflective pixel electrode and the transmissive pixel electrode does not exist in the transparent area.

11. The reflective transmission type thin film transistor liquid crystal display of claim 1, wherein the passivation layer is made with photosensitive transparent insulator and the surface of the passivation layer is treated to take embossment which forms micro lens for focusing light.

12. A Reflective transmission type thin film transistor liquid crystal display

(TFT LCD) comprising:

a glass substrate;

at least one thin film transistor on the substrate for controlling a pixel;

5 a first type pixel electrode which is deposited and patterned concurrently with gate of the thin film transistor, comprising upper non-oxidizing metal layer removed in transparent window region and lower transparent conductor layer;

a passivation layer which is formed over the thin film transistor and the first type pixel electrode and which has a contact hole at a source contact region and a hole at the transparent window and neighboring region; and

10 a second type pixel electrode which is formed over the passivation layer, is patterned to cover whole pixel area except the transparent window region, and is connected with a source electrode of the thin film transistor through the contact hole and connected with the non-oxidizing metal layer of the first type pixel electrode at the neighboring region.

15 13. The reflective transmission type thin film transistor liquid crystal display of claim 12, wherein the passivation layer is made with photosensitive transparent insulator

20 14. The reflective transmission type thin film transistor liquid crystal display of claim 13, wherein at least at some part the surface of the passivation layer is treated to take embossment which forms micro lens for focusing light.

15. The reflective transmission type thin film transistor liquid crystal display of claim 12, wherein the thickness of the passivation layer corresponds to the a quarter of wavelength of light in liquid crystal.

5 16. The reflective transmission type thin film transistor liquid crystal display of claim 12, wherein the non-oxidizing metal is one selected form the group consisting of chromium and tungsten-molybdenum.

## Abstract of the Disclosure

The present invention provide a reflective transmission type TFT LCD wherein

5 each of the reflective pixel electrode and the transmissive pixel electrode is connected directly to a source electrode of a thin film transistor, or the transmissive pixel electrode are concurrently formed with gate electrode and made with double layer of transparent conduction layer and metal layer which can be used as parameter conduction layer between the transparent conduction layer and the reflective pixel electrode. According

10 to one aspect of the present invention, the reflective transmission type thin film transistor liquid crystal display (TFT LCD) comprises a glass substrate, at least one thin film transistor on the substrate for controlling a pixel, passivation layer having at least one contact hole in a source region of the thin film transistor, a transmissive pixel electrode which is formed on the passivation layer and is connected with a source

15 electrode of the source region through a contact hole, a reflective pixel electrode which is formed on the passivation layer and is connected with the source electrode of the source region through a contact hole. And the pixel area is composed of a transparent area in which only the transmissive pixel electrode of whole pixel electrode exist and a reflective area in which the reflective pixel electrode exist.

Fig. 1

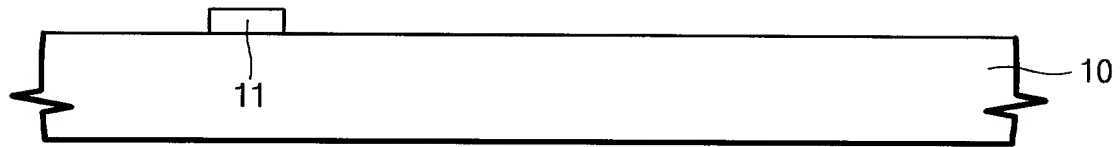


Fig. 2

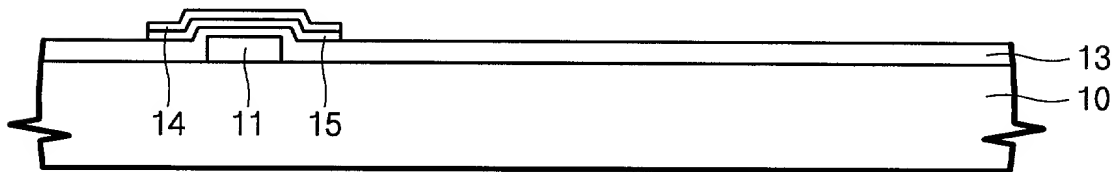


Fig. 3

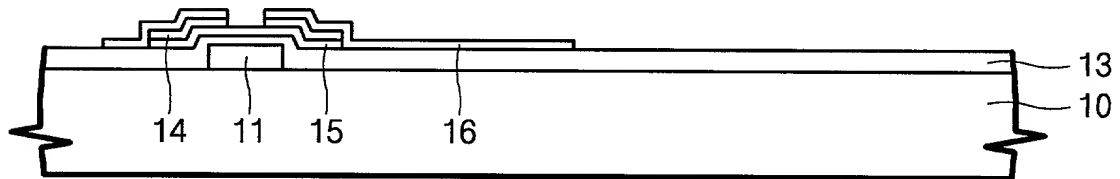


Fig. 4

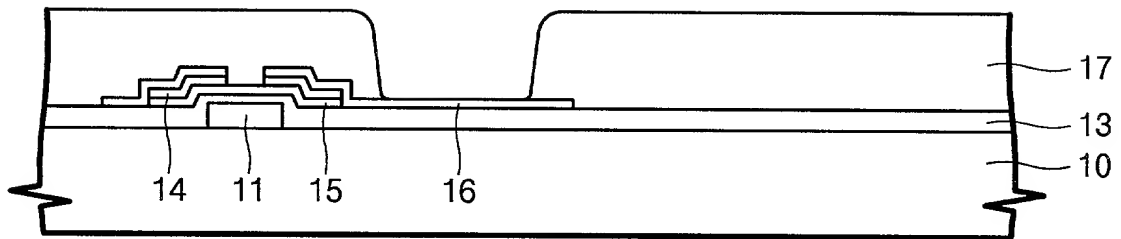


Fig. 5

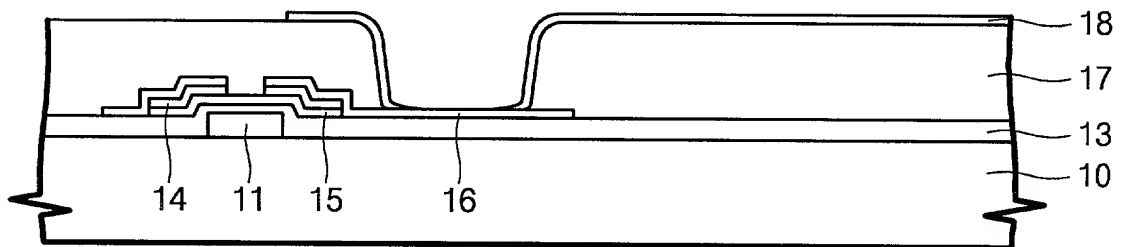


Fig. 6

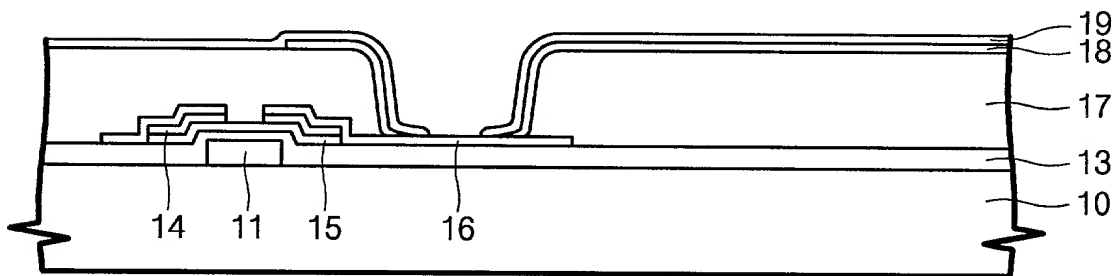


Fig. 7

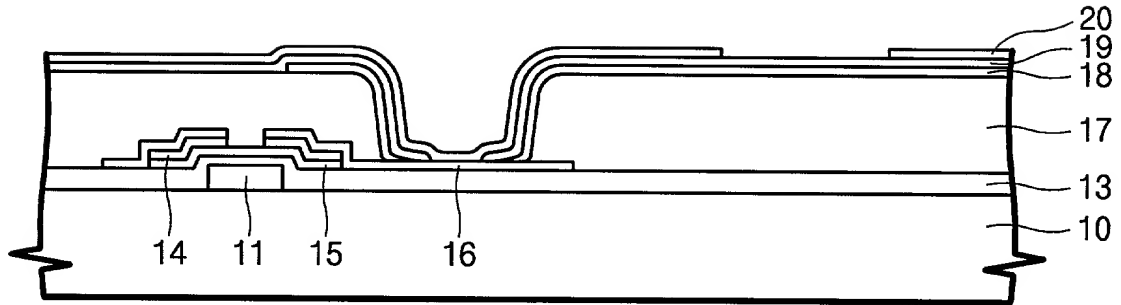


Fig. 8

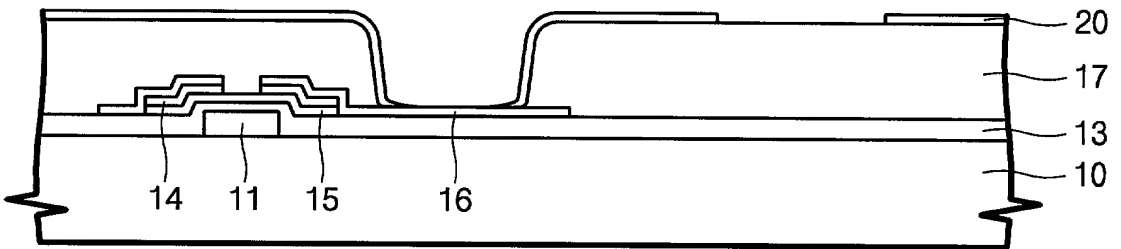


Fig. 9

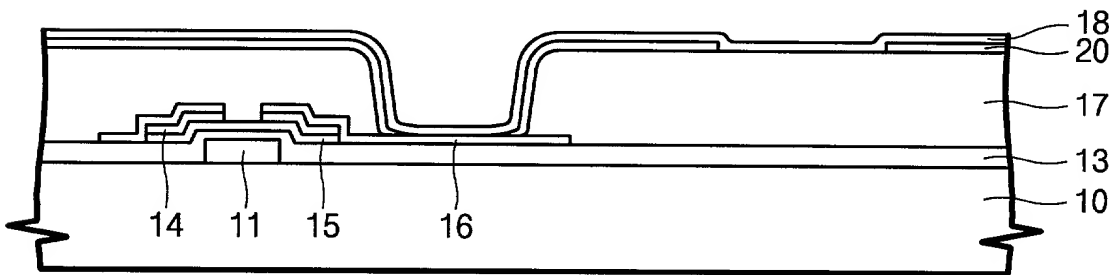


Fig. 10

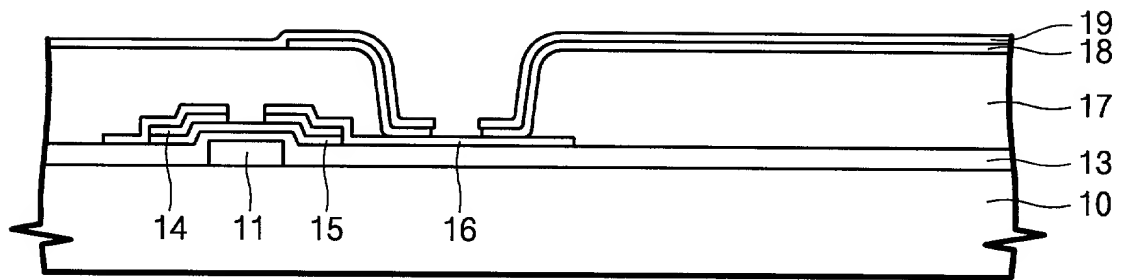


Fig. 11

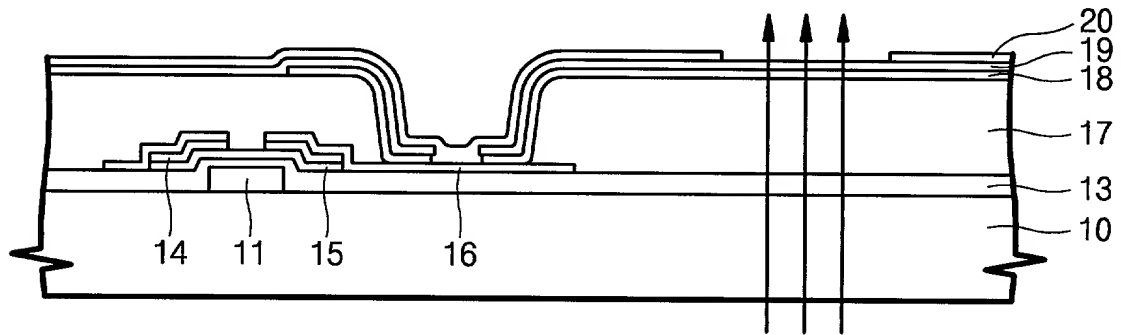


Fig. 12

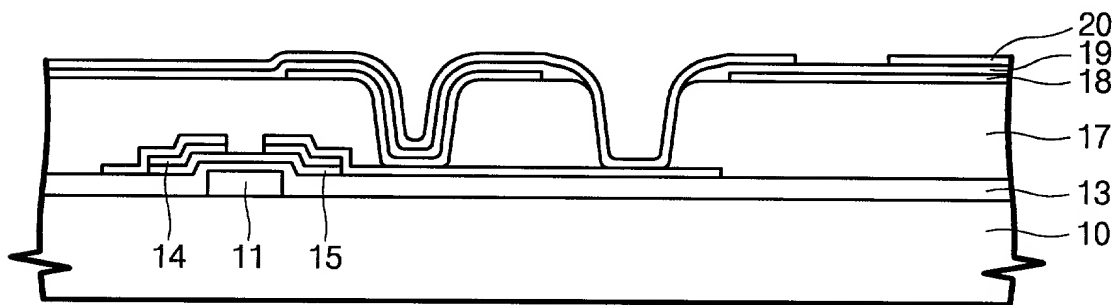




Fig. 13

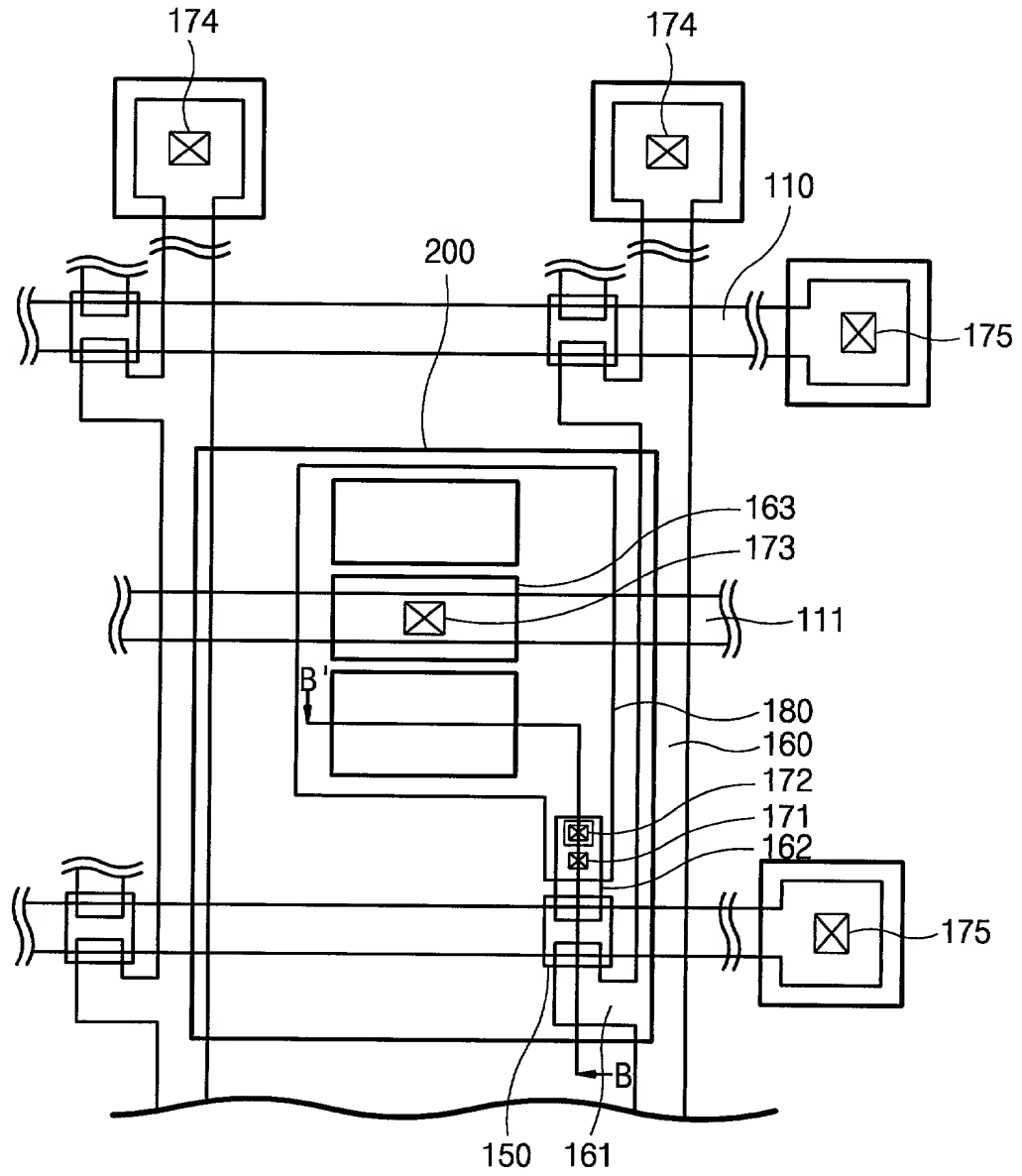
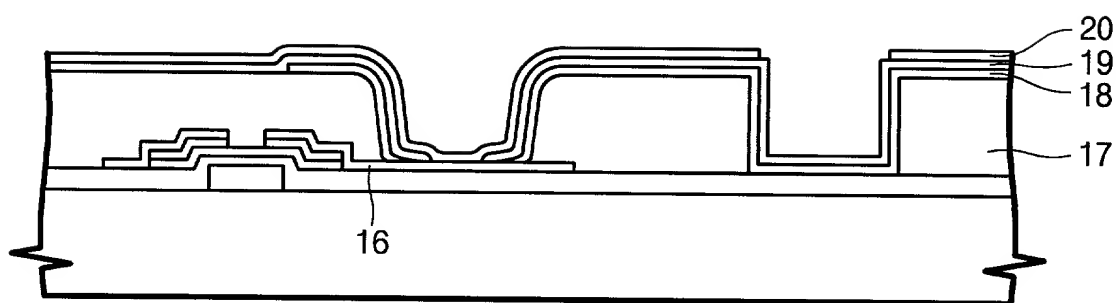


Fig. 14A



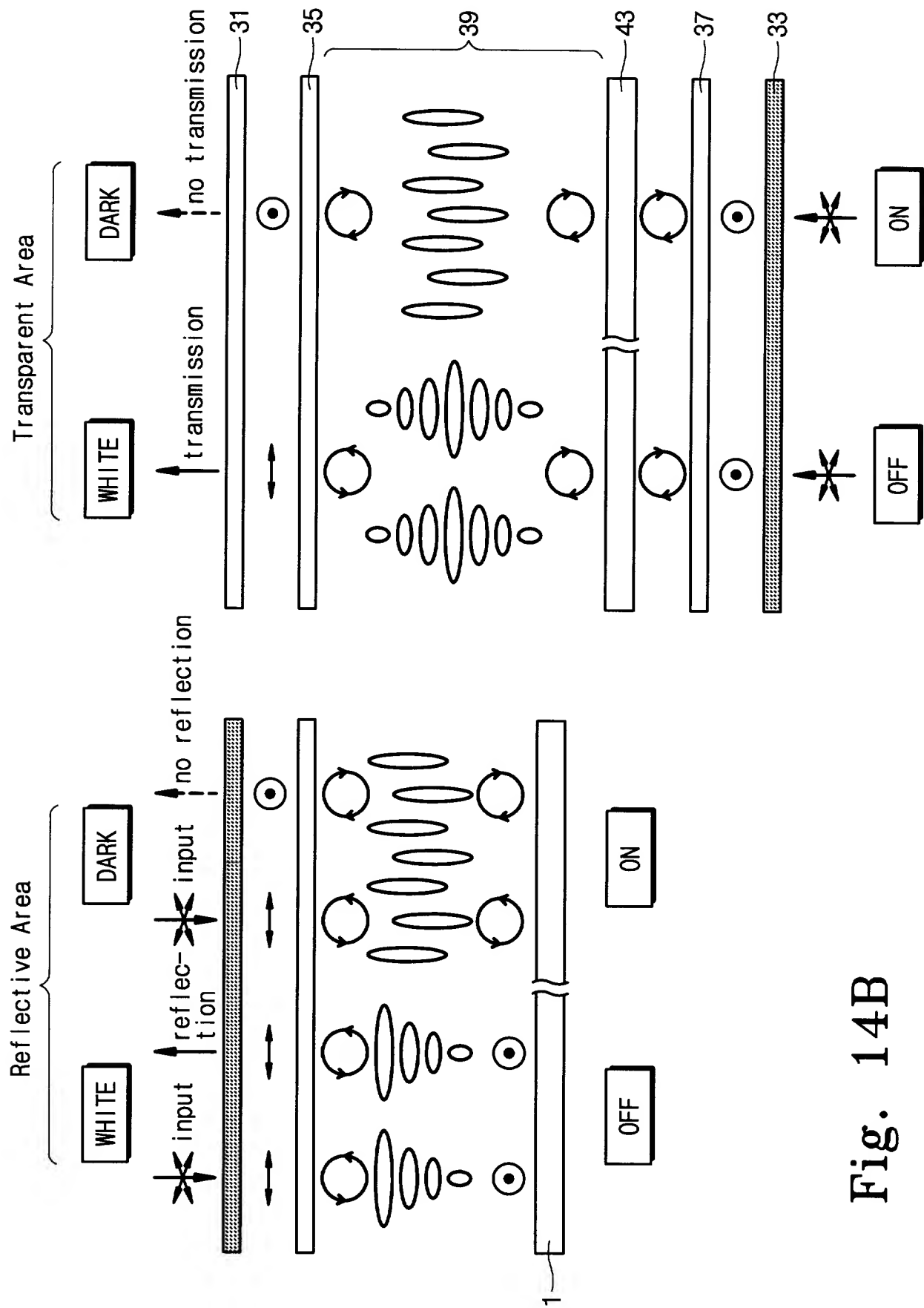


Fig. 14B

Fig. 15

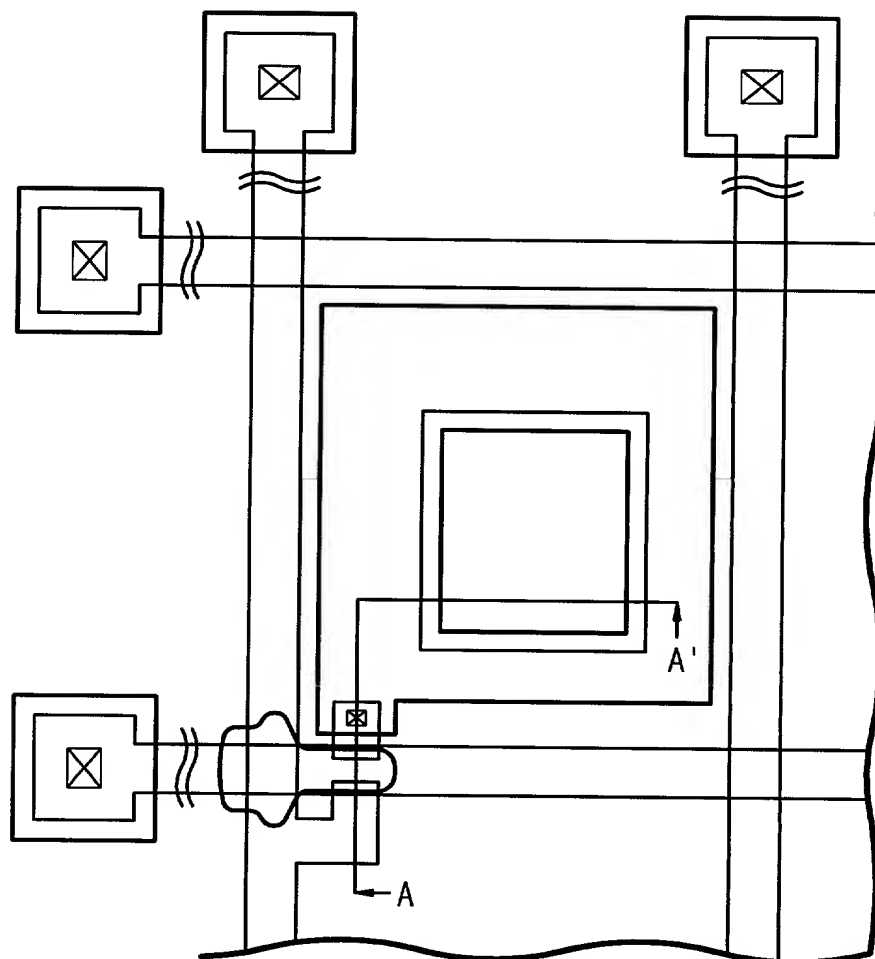


Fig. 16

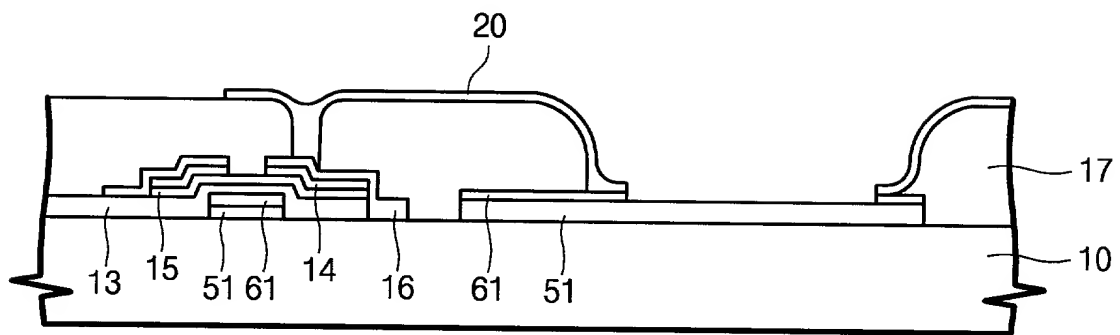


Fig. 17

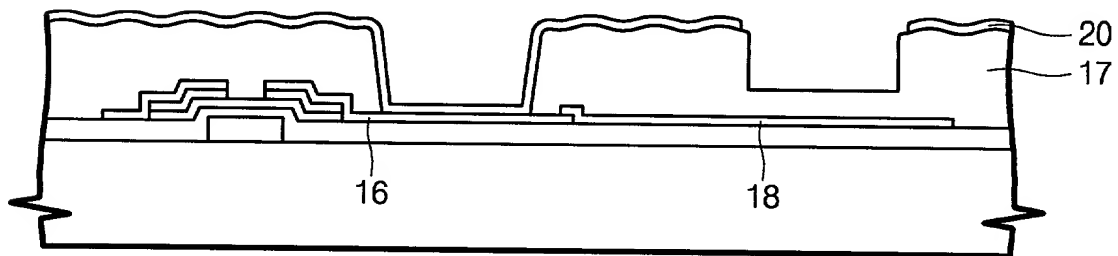


Fig. 18

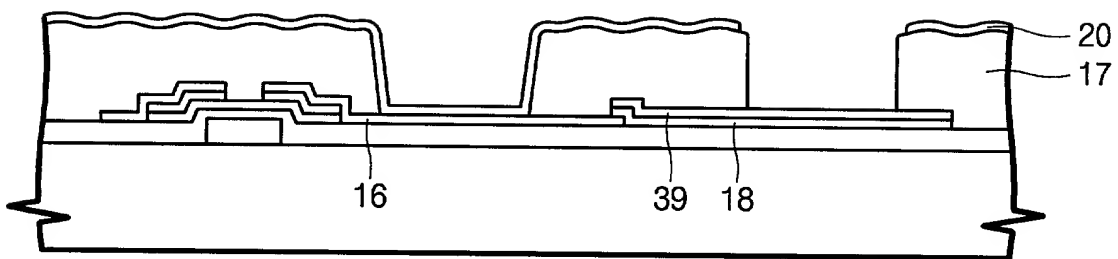


Fig. 19

